



ELECTRONICS ENGINEERING DEPARTMENT

इलेक्ट्रॉनिक्स इंजीनियरिंग डिपार्टमेंट

SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT

सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नोलॉजी, सूरत

Ref. No.

दिनांक:06/05/2024

With reference to advertisement for the post of research personnel for the project titled "*Design and Characterization of Radiation Hardened Standard Cell Library using Nanosheet Field Effect Transistors: Improving on Design Margins/Reliability*" on dated 28/03/2024, funded by SERB – DST, Govt. of India, the Written Examination / Personal Interview will be conducted at the Electronics Engineering Department, Sardar Vallabhbhai National Institute of Technology, Surat.

Short Listed Candidate for Written Examination / Personal Interview

S. No.	Name of the Candidate	Eligibility	Remarks
1	Sachin Kansal	Provisionally Eligible	<ul style="list-style-type: none">• Marksheets of all semesters for UG/PG are required.• NOC from Current Employer is required
2	Jyoti Prakash Purohit	Provisionally Eligible	<ul style="list-style-type: none">• Marksheets of all semesters for UG/PG are required.
3	Vaishali Jatav	Provisionally Eligible	<ul style="list-style-type: none">• Marksheets of all semesters for UG/PG are required.• PG Degree Certificate is required.

NOTE

5. Please bring all the original documents such as Marksheets, Degree, experience Certificates, NOC etc. for verification.
6. Candidates are required to bring one hard copy of duly filled Application along with all the photo copies of all the necessary documents.
7. All the candidates are required to report on 11/05/2024 at Department of Electronics Engineering, SVNIT Surat by 09:00 AM.
8. No TA/DA will be paid for the visit.

Abhishek Acharya

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Principle Investigator